



10-04-05

JFW

**THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/803,203 Confirmation No.: 5841  
First Named Inventor: Bulucea, Constantin Filing Date: 17 March 2004  
Group Art Unit: 2826 Examiner: Quach, T. N.  
Atty. Docket No.: NS-5737 US  
Title: Configuration and Fabrication of Semiconductor Structure Having  
N-channel Channel-junction Field-effect Transistor  
Assignee(s): National Semiconductor Corporation

Mountain View, California  
3 October 2005

**COMMISSIONER FOR PATENTS**  
**PO Box 1450**  
**Alexandria, Virginia 22313-1450**

**RESPONSE TO RESTRICTION REQUIREMENT**

Sir:

Responsive to the Restriction Requirement mailed 2 September 2005 for the above patent application, Applicants' Attorney provisionally elects device Claims 1 - 42 in Group I for prosecution in this application.

Please telephone Applicants' Attorney at 650-964-9767 if there are any questions.

**EXPRESS MAIL LABEL NO.:**

**EV 500 311 205 US**

Respectfully submitted,

*Ronald J. Meetin*

Ronald J. Meetin  
Attorney for Applicant(s)  
Reg. No. 29,089

210 Central Avenue  
Mountain View, CA 94043-4869

Ronald J. Meetin  
Attorney at Law  
210 Central Avenue  
Mountain View, CA  
94043-4869

Tel.: 650-964-9767  
Fax: 650-964-9779